

WEST Search History

DATE: Friday, November 12, 2004

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	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L24	(l18 or l14) and (l22 or l21)	13
<input type="checkbox"/>	L23	l18 or l14 and (l22 or l21)	160757
<input type="checkbox"/>	L22	(current near3 (driver\$ or transmitter))and (class\$ near4 (a near2 b))	99
<input type="checkbox"/>	L21	(current near3 (driver\$ or transmitter))and (emission\$ near5 (power near2 consumption\$))	34
<input type="checkbox"/>	L20	L18 and(l11 or l12)	10
<input type="checkbox"/>	L19	L18 adn (l11 or l12)	174398
<input type="checkbox"/>	L18	333/\$4.ccls. or 327/\$4.ccls.	160751
<input type="checkbox"/>	L17	6594304.pn.	2
<input type="checkbox"/>	L16	L14 and l12	7
<input type="checkbox"/>	L15	L14 and l11	6
<input type="checkbox"/>	L14	375/257.ccls. or 375/258.ccls.	1286
<input type="checkbox"/>	L13	L12 and l11	13
<input type="checkbox"/>	L12	(line near3 (driver\$ or transmitter))and (emission\$ near5 (power near2 consumption\$))	64
<input type="checkbox"/>	L11	(line near3 (driver\$ or transmitter))and (class\$ near4 (a near2 b))	95
<input type="checkbox"/>	L10	(differentail near4 transmitter\$) and (power near4 mode\$) and (class\$ near4 (a near2 b))	0
<input type="checkbox"/>	L9	(differentail near4 transmitter\$) and (power near4 mode\$) and (emission\$ near4 mode\$)	0
<input type="checkbox"/>	L8	(differentail near2 current\$ near2 transmitter\$) and (power near4 mode\$) and (emission\$ near4 mode\$)	0
<input type="checkbox"/>	L7	(differentail near2 current\$ near2 transmitter\$) and (class\$ near2 (a near2 b))	0
<input type="checkbox"/>	L6	(differentail near2 current\$ near2 driver\$) and (class\$ near2 (a near2 b))	0
<input type="checkbox"/>	L5	(differentail near2 current\$ near2 driver\$) and (power near4 mode\$) and (emission\$ near4 mode\$)	0
<input type="checkbox"/>	L4	(differenail near2 current\$ near2 driver\$) and (power near4 mode\$) and (emission\$ near4 mode\$)	0
<input type="checkbox"/>	L3	(differenail near2 current\$ near2 driver\$) and ((power near4 mode\$) same (emission\$ near4 mode\$))	0
<input type="checkbox"/>	L2	(differenail near2 current\$ near2 driver\$) same ((power near mode\$) same (emission\$ near2 mode\$))	0

<input type="checkbox"/>	L1	(differenail near2 current\$ near2 driver\$) same ((multiple or plurral\$3) near3 mode\$)	0
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END OF SEARCH HISTORY

Day : Wednesday

Date: 11/10/2004

Time: 14:55:30

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = CHAN

First Name = KEVIN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>60502790</u>	Not Issued	159	09/12/2003	INTEGRATED GIGABIT ETHERNET CONTROLLER	CHAN, KEVIN
<u>60451138</u>	Not Issued	159	02/28/2003	METHOD AND SYSTEM FOR AN "ALWAYS-ON INTERNET DEVICE"	CHAN, KEVIN HSUNKO
<u>60385960</u>	Not Issued	159	06/04/2002	AUTOMATIC MASSAGE CHAIR DEVICE	CHAN, KEVIN K.
<u>60365806</u>	Not Issued	159	03/21/2002	GIGABIT ETHERNET TRANSCEIVER	CHAN, KEVIN
<u>60339089</u>	Not Issued	159	12/10/2001	LOW TEMPERATURE BI-CMOS COMPATIBLE PROCESS FOR MEMS RF RESONATORS AND FILTERS	CHAN, KEVIN K.
<u>60243483</u>	Not Issued	159	10/26/2000	AUTO-MDIX LINE DRIVER WITH POWER DOWN LOOPBACK PROTECTION	CHAN, KEVIN T.
<u>60202660</u>	Not Issued	159	05/08/2000	METHOD FOR MANUFACTURING DEVICE SUBSTRATE WITH METAL BACK-GATE AND STRUCTURE FORMED THEREBY	CHAN, KEVIN K.
<u>60185538</u>	Not Issued	159	02/28/2000	HIGH-SPEED TRANSMISSION SYSTEM FOR OPTICAL CHANNELS	CHAN, KEVIN
<u>10650400</u>	Not Issued	030	08/28/2003	STRAINED SILICON-CHANNEL MOSFET USING A DAMASCENE GATE PROCESS	CHAN, KEVIN K.
<u>10641691</u>	Not Issued	030	08/14/2003	METHOD AND SYSTEM FOR AN "ALWAYS-ON" INTERNET DEVICE	CHAN, KEVIN HSUNKO
<u>10625505</u>	Not Issued	030	07/22/2003	TECHNIQUES FOR AUTOMATED SWEEPING OF PARAMETERS IN COMPUTER-AIDED DESIGN TO	CHAN, KEVIN

				ACHIEVE OPTIMUM PERFORMANCE AND RESOURCE USAGE	
<u>10616962</u>	Not Issued	093	07/11/2003	METHOD OF REDUCING POLYSILICON DEPLETION IN A POLYSILICON GATE ELECTRODE BY DEPOSITING POLYSILICON OF VARYING GRAIN SIZE	CHAN, KEVIN K.
<u>10612729</u>	Not Issued	020	07/02/2003	METHOD AND SYSTEM FOR AUTOMATIC MEDIA DEPENDENT INTERFACE RECONFIGURATION AND REPAIR	CHAN, KEVIN T.
<u>10612083</u>	Not Issued	030	07/02/2003	METHOD AND SYSTEM FOR A REDUCED EMISSIONS DIRECT DRIVE TRANSMITTER FOR UNSHIELDED TWISTED PAIR (UTP) APPLICATIONS	CHAN, KEVIN T.
<u>10612025</u>	Not Issued	030	07/02/2003	METHOD AND SYSTEM FOR SECURE AUTOMATIC MEDIA DEPENDENT INTERFACE RECONFIGURATION AND REPAIR	CHAN, KEVIN T.
<u>10605128</u>	Not Issued	071	09/10/2003	CAPACITOR AND FABRICATION METHOD USING ULTRA-HIGH VACUUM CVD OF SILICON NITRIDE	CHAN, KEVIN K.
<u>10461821</u>	Not Issued	094	06/13/2003	FULLY-DEPLETED SOI MOSFETS WITH LOW SOURCE AND DRAIN RESISTANCE AND MINIMAL OVERLAP CAPACITANCE USING A RECESSED CHANNEL DAMASCENE GATE PROCESS	CHAN, KEVIN K.
<u>10459182</u>	Not Issued	020	06/10/2003	VARIABLE BIT RATE VIDEO CODEC USING ADAPTIVE TRACKING FOR VIDEO CONFERENCING	CHAN, KEVIN
<u>10456005</u>	Not Issued	030	06/06/2003	APPARATUS AND METHOD FOR PRESENCE-BASED CALL ROUTING USING COMPUTERS	CHAN, KEVIN
<u>10437930</u>	Not Issued	041	05/15/2003	SELF-ALIGNED SILICIDE PROCESS FOR SILICON SIDEWALL SOURCE AND DRAIN CONTACTS AND STRUCTURE FORMED THEREBY	CHAN, KEVIN K.

<u>10419888</u>	Not Issued	041	04/22/2003	METHOD AND STRUCTURE FOR ULTRA-LOW CONTACT RESISTANCE CMOS FORMED BY VERTICALLY SELF-ALIGNED COSI2 ON RAISED SOURCE DRAIN SI/SIGE DEVICE	CHAN, KEVIN K.
<u>10411727</u>	<u>6762101</u>	150	04/11/2003	DAMASCENE DOUBLE-GATE FET	CHAN, KEVIN K.
<u>10392831</u>	Not Issued	030	03/21/2003	PHYSICAL LAYER DEVICE HAVING AN ANALOG SERDES PASS THROUGH MODE	CHAN, KEVIN T.
<u>10386880</u>	<u>6809005</u>	150	03/12/2003	METHOD TO FILL DEEP TRENCH STRUCTURES WITH VOID-FREE POLYSILICON OR SILICON	CHAN, KEVIN K.
<u>10374866</u>	Not Issued	041	02/25/2003	SHALLOW TRENCH ISOLATION STRUCTURE FOR STRAINED SI ON SIGE	CHAN, KEVIN KOK
<u>10372778</u>	Not Issued	092	02/26/2003	PROGRAMMABLE GAIN AMPLIFIER WITH GLITCH MINIMIZATION	CHAN, KEVIN T.
<u>10334176</u>	Not Issued	030	12/30/2002	LOW TEMPERATURE FUSION BONDING WITH HIGH SURFACE ENERGY USING A WET CHEMICAL TREATMENT	CHAN, KEVIN K.
<u>10328258</u>	Not Issued	083	12/23/2002	NITRIDE-ENCAPSULATED FET (NNCFET)	CHAN, KEVIN K.
<u>10162974</u>	Not Issued	041	06/05/2002	FULLY INTEGRATED ETHERNET TRANSMITTER ARCHITECTURE WITH INTERPOLATING FILTERING	CHAN, KEVIN T.
<u>10156782</u>	<u>6690072</u>	150	05/24/2002	METHOD AND STRUCTURE FOR ULTRA-LOW CONTACT RESISTANCE CMOS FORMED BY VERTICALLY SELF-ALIGNED COSI2 ON RAISED SOURCE DRAIN SI/SIGE DEVICE	CHAN, KEVIN K.
<u>10124967</u>	<u>6594304</u>	150	04/18/2002	ADAPTIVELY CONFIGURABLE CLASS-A/CLASS-B TRANSMIT DAC FOR TRANSCEIVER EMISSION AND POWER CONSUMPTION CONTROL	CHAN, KEVIN T.
<u>10119799</u>	<u>6580132</u>	150	04/10/2002	DAMASCENE DOUBLE-GATE FET	CHAN, KEVIN K.
<u>10102365</u>	Not Issued	094	03/20/2002	SELF-ALIGNED NANOTUBE	CHAN,

				FIELD EFFECT TRANSISTOR AND METHOD OF FABRICATING SAME	KEVIN K.
<u>10095233</u>	Not Issued	121	03/11/2002	REDUCTION OF AGGREGATE EMI EMISSIONS OF MULTIPLE TRANSMITTERS	CHAN, KEVIN T.
<u>10093789</u>	<u>6821864</u>	150	03/07/2002	NOVEL METHOD TO ACHIEVE INCREASED TRENCH DEPTH, INDEPENDENT OF CD AS DEFINED BY LITHOGRAPHY	CHAN, KEVIN K.
<u>10091099</u>	<u>6690742</u>	150	03/05/2002	ADAPTIVELY CONFIGURABLE CLASS-A/CLASS-B TRANSMIT DAC FOR TRANSCEIVER EMISSION AND POWER CONSUMPTION CONTROL	CHAN, KEVIN T.
<u>10087239</u>	<u>6744831</u>	150	03/01/2002	ADAPTIVE ELECTRONIC TRANSMISSION SIGNAL CANCELLATION APPARATUS FOR FULL DUPLEX COMMUNICATION	CHAN, KEVIN T.
<u>10084550</u>	<u>6660598</u>	150	02/26/2002	METHOD OF FORMING A FULLY-DEPLETED SOI (SILICON-ON-INSULATOR) MOSFET HAVING A THINNED CHANNEL REGION	CHAN, KEVIN K.
<u>09969793</u>	<u>6538508</u>	150	10/04/2001	PROGRAMMABLE GAIN AMPLIFIER WITH GLITCH MINIMIZATION	CHAN, KEVIN T.
<u>09797428</u>	Not Issued	041	02/28/2001	SYSTEM AND METHOD FOR HIGH SPEED COMMUNICATIONS USING DIGITAL SIGNAL PROCESSING	CHAN, KEVIN T.
<u>09730976</u>	<u>6692256</u>	150	12/06/2000	INTERACTIVE TUTORIAL	CHAN, KEVIN
<u>09712264</u>	<u>6503833</u>	150	11/15/2000	SELF-ALIGNED SILICIDE (SALICIDE) PROCESS FOR STRAINED SILICON MOSFET ON SIGE AND STRUCTURE FORMED THEREBY	CHAN, KEVIN KOK
<u>09597765</u>	<u>6444592</u>	150	06/20/2000	INTERFACIAL OXIDATION PROCESS FOR HIGH-K GATE DIELECTRIC PROCESS INTEGRATION	CHAN, KEVIN K.
<u>09596725</u>	<u>6262464</u>	150	06/19/2000	ENCAPSULATED MEMS BAND-PASS FILTER FOR INTEGRATED CIRCUITS	CHAN, KEVIN K.
<u>09569306</u>	Not Issued	083	05/11/2000	SELF-ALIGNED SILICONE	CHAN,

				PROCESS FOR LOW RESISTIVITY CONTACTS TO THIN FILM SILICON-ON-INSULATOR MOSFETS	KEVIN KOK
<u>09568520</u>	<u>6389077</u>	150	05/09/2000	ADAPTIVELY CONFIGURABLE CLASS-A/CLASS-B TRANSMIT DAC FOR TRANSCEIVER EMISSION AND POWER CONSUMPTION CONTROL	CHAN, KEVIN T.
<u>09550990</u>	Not Issued	094	04/17/2000	SCALABLE MOS FIELD EFFECT TRANSISTOR	CHAN, KEVIN KOK
<u>09536016</u>	<u>6724449</u>	150	03/27/2000	VERTICAL ALIGNED LIQUID CRYSTAL DISPLAY AND METHOD USING DRY DEPOSITED ALIGNMENT LAYER FILMS	CHAN, KEVIN KOK
<u>09512721</u>	<u>6218315</u>	150	02/24/2000	HTO (HIGH TEMPERATURE OXIDE) DEPOSITION FOR CAPACITOR DIELECTRICS	CHAN, KEVIN K.
<u>09501226</u>	<u>6339002</u>	150	02/10/2000	METHOD UTILIZING CMP TO FABRICATE DOUBLE GATE MOSFETS WITH CONDUCTIVE SIDEWALL CONTACTS	CHAN, KEVIN K.
<u>09501225</u>	<u>6333247</u>	150	02/10/2000	TWO-STEP MOSFET GATE FORMATION FOR HIGH-DENSITY DEVICES	CHAN, KEVIN K.

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